Low-threshold, single-transverse-mode, 940-nm vertical-cavity surface-emitting laser with a mode filter and half-wavelength cavity

Q.H. Ren, J. Wang, M. Yang, H.J. Wang, Z. Cheng, Y.Q. Huang, X.M. Ren, H.M. Ji, S. Luo

Abstract. **We demonstrate a structure for a vertical-cavity surfaceemitting laser (VCSEL) with a mode filter and half-wavelength** $(\lambda/2)$ cavity. The effects of $\lambda/2$ - and λ -cavities on the threshold cur**rent are studied. The thickness and etched diameter of the mode filter are optimised to achieve single-transverse-mode output. The results show that when changing from** λ **-cavity to** λ **/2-cavity, the threshold current of the VCSEL decreases by 30.7% (from 0.85 mA to 0.65 mA). In addition, for the mode-filter thickness of 80 nm, and the etched diameter of 2.5 mm, the side mode suppression ratio reaches 90 dB, which meets the requirement of single-transversemode output. The VCSELs with the optimised design can overcome many difficulties and have great potential in 3D face recognition.**

Keywords: VCSELs, mode filter, single-transverse-mode, halfwavelength cavity, threshold current.

1. Introduction

Vertical-cavity surface-emitting lasers (VCSELs) are widely used in optical communications, optical interconnects, and optical information processing due to their low power consumption, effective coupling to fiber, low threshold current, and high power conversion efficiency [1, 2]. At present, the vast majority of VCSELs are employed in the field of electronic consumption, including proximity sensing in mobile phones, scanning of objects within a few meters, and laser radar systems in the autonomous driving that can detect obstacles more than 100 metres ahead [3 –6].

In recent years, with the progress of research on VCSELs, the optical power, modulation bandwidth and reliability of VCSELs have been continuously improved. However, the application in optical communications is limited due to multitransverse modes of VCSELs [7]. Therefore, it is vital to improve the single-transverse-mode output characteristics of VCSELs. So far, various methods have been proposed to realise single-transverse-mode output, such as, photonic crystal structure, small oxide aperture structure and mode filter structure [8 –13]. In 2013, Tan et al. [14] realised a VCSEL with single-transverse-mode emission through a photonic

Q.H. Ren, J. Wang, M. Yang, H.J. Wang, Z. Cheng, Y.Q. Huang, X.M. Ren State Key Laboratory of Information Photonics and Optical Communications, Beijing University of Posts and Telecommunications, Beijing 100876, China; e-mail: wangjun12@bupt.edu.cn; **H.M. Ji, S. Luo** Center of Materials Science and Optoelectronics Engineering, University of Chinese Academy of Sciences, Beijing 100049, China

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crystal structure and demonstrated 25 Gbs–1 error-free transmission through a 1-km-long multimode fibre cable OM4 at a current density of 5.4 kA cm⁻². However, due to the complexity of photonic crystal it is difficult to realise this approach in experiments. In the same year, Safaisini et al. [15] realised a single-transverse-mode VCSEL with an oxide aperture of 3 um. Unfortunately, as the oxide-confined aperture becomes smaller, the differential resistance, current density and internal temperature of the VCSEL inevitably increase. To solve these problems, in 2015, Larsson et al. [16] used a mode filter structure to achieve single-transverse-mode transmission. Previous research on single-transverse-mode VCSELs using a mode filter are mainly focused on the field of optical communications. However, there are only a few studies on singletransverse-mode VCSELs in the field of consumer electronics. With the increasing application of 940 nm VCSELs in mobile devices, eye safety and the stability of 3D recognition have attracted extensive attention and need to be improved urgently $[17-19]$.

In addition, in order to reduce the power consumption in practical applications, the threshold current of the VCSEL is also the focus of attention. At present, there are three main methods for reducing the threshold current, including a small-sized oxide aperture structure, a photonic crystal structure, and a strain-compensated quantum well structure [20, 21]. In 2011, Lott et al. [22] designed an oxide aperture of 3 µm to achieve an 850 nm VCSEL output with a threshold current below 0.5 mA. In 2012, by using a photonic crystal structure, Xie et al. [23] successfully prepared an 850 nm VCSEL with a threshold current of 0.9 mA. In 2019, Yu et al. [24] finally realised a 940 nm VCSEL with the threshold current and slope efficiency of 0.95 mA and 0.96 W A^{-1} , respectively, using the gain characteristics of InGaAs/ GaAsP strain-compensated quantum wells. Moreover, Mutig et al. [25] calculated the optical confinement factor of the VCSEL with a λ /2-cavity, equal to 5.1%, which is 70% larger than that of the VCSEL with a 3*l*/2-cavity. In theory, we know that the threshold current can be reduced by increasing the optical confinement factor, so it seems feasible to reduce the threshold current by shortening the cavity length [25].

Although many characteristics of VCSELs have been greatly improved, there are still some problems in the application of portable devices (mobile devices, wearable devices, notebook computers) [26, 27]. Thus, a 940 nm VCSEL with a *l*/2-cavity and mode-filter structure has been proposed to solve these problems. By using a $\lambda/2$ -cavity, the threshold current and energy consumption can be effectively reduced, and the service life can be extended as well. The mode-filter structure can control the optical mode output, and its output power can be very small, and the damage to the human eyes is negligible. Up to date, there have been few reports on the theoretical design and optimisation of 940-nm VCSELs used for 3D recognition, which require a low threshold and singletransverse-mode.

In this paper, we proposed a 940-nm VCSEL with a *l*/2 cavity length and mode-filter structure. The threshold currents of the VCSELs with *l*/2- and *l*-cavities are investigated. The optical field distribution of the VCSELs is calculated by three-dimensional finite-difference time-domain method. The thickness and etched diameter of the mode filter are optimised to achieve a single-transverse-mode output. In addition, we analyzed the reasons for the threshold current reduction from the perspective of the optical confinement factor as well. The results show that compared with the traditional VCSELs, the optimised VCSEL has a lower threshold current and a higher output power. The threshold current is reduced from 0.85 mA to 0.65 mA, that is by 30.7%. The output power rose from 1.25 mW to 1.4 mW, with an increase of 10.7%. The thickness and etched diameter of the optimised mode filter are 80 nm and 2.5 µm, respectively. The side mode suppression ratio reaches 90 dB, which enables single-transverse-mode output. The optimised VCSEL is more suitable for practical application. Furthermore, it will promote the development of 940 nm VCSELs in the field of consumer electronics for near-infrared light sources in 3D recognition.

2. Device structure

The device structure of the VCSEL is illustrated in Fig. 1. The n-type (doping, 2×10^{18} cm⁻³) mirror containing 34 pairs of $\text{Al}_{0.9}\text{Ga}_{0.1}\text{As/GaAs}$ layers is designed as the bottom distributed-Bragg-reflector (DBR). The active region is composed of three pairs of $In_{0.11}Ga_{0.89}As$ (8 nm)/ $Al_{0.2}Ga_{0.8}As$ (8 nm) quantum wells, sandwiched between two 51-nm cladding layers (from $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ to $\text{Al}_{0.9}\text{Ga}_{0.1}\text{As}$). The upper DBR mirror with 22.5 pairs of p-type (doping, 4×10^{18} cm⁻³) $\text{Al}_{0.9}\text{Ga}_{0.1}\text{As/GaAs}$ layers was grown on the active region. The cavity length is designed to be *l*/2 [28]. In detail, for the first pair of the upper DBR on the active region, it is a 66.4-nm GaAs high-index layer with the optical thickness of *l*/4. In sequence, there is a 47.8-nm $Al_{0.9}Ga_{0.1}As layer and a 30-nm$ $Al_{0.98}Ga_{0.02}As layer consisting of a low-index layer with the$ optical thickness of λ /4. A separate $Al_{0.98}Ga_{0.02}As$ layer is used to form a 6-um aperture to confine the current and optical field, and effectively reduce diffraction loss. In addition, a GaAs layer is added between the p-type DBR and the ohmic contact layer for post-production of the mode-filter structure. Compared to unoxidised regions, the effective index of the oxidised region is reduced. Therefore, the oxidation-limited VCSEL structure can be regarded as a fibre model [29].

In this work, the optical field distribution and reflectivity of the VCSEL are simulated by the finite-difference timedomain method. The boundary condition is a perfectly matched layer. The thermal effects of the VCSEL are considered when analysing the photoelectric characteristics, including the Joule heat generated by differential resistance around the oxide layer, the absorption heat in doped semiconductor material in the DBRs, and nonradiative recombination heat generated by electrons and holes in the active region. The coefficients referenced from literature [29] are as follows: the internal optical loss is 10 cm–1; the spontaneous emission coefficient is 2×10^{10} cm³ s⁻¹; and the nonradiative and Auger recombination coefficients are 1×10^8 s⁻¹ and 1.5×10^{-29} cm⁶ s⁻¹, respectively. Other parameters of the materials used in the simulation are listed in Table 1.

3. Results and discussion

To optimise the VCSEL performance, we investigated the relationship between its photoelectric properties and material structure. The threshold current of the VCSEL with *l*/2- and *l*-cavities are compared, and then the thickness and etched diameter of the GaAs layer are optimised to form the mode filter.

Figure 2 shows the relationship between the output power and the threshold current. For a cavity length of *l*/2, the threshold current is 0.65 mA, and the output power reaches a

Figure 1. VCSEL structure: *d* and *b* are the thickness and diameter of the etched region of the mode filter; n_1 and n_2 are the effective refractive indices of the core region and the cladding region, respectively.

Table 1.

maximum of 1.4 mW at 6 mA. For a *l*-cavity, the threshold current is 0.85 mA, and the output power reaches a maximum of 1.25 mW at a current of 5.5 mA. Thus, a VCSEL with a λ /2-cavity can reduce the threshold current by 30.8% compared to that with a *l*-cavity.

Figure 2. Output power vs. current for the VCSEL with (1) $\lambda/2$ - and (2) λ -cavities.

In addition, the optical confinement factor of the both VCSELs is compared to further understand the above results [30, 31]. Figure 3a demonstrates the refractive index and the simulated optical field distribution in the traditional VCSEL with a λ -cavity. In the active region, the optical intensity peak in the centre is lower than that on both sides. Figure 3b shows the case of the VCSEL with *l*/2-cavity. One can see that the optical intensity peak in the centre is higher than that on both sides; therefore, the optical field distribution is more advantageous for the *l*/2-cavity, because the optical intensity peak in the centre is much stronger than that for the *l*-cavity. Consequently, the overlap of the lasing mode with the active region increases, leading to a strong increase in the optical confinement factor. The optical confinement factor for the VCSEL with the λ /2-cavity is 3.44%, which is 12.5% lager than the value of the confinement factor for VCSELs based on the λ -cavity, where it is only 3%. The results show that in the VCSEL with a λ /2-cavity the optical confinement factor is higher and the threshold current is lower than those in the VCSE with a *l*-cavity.

Second, we optimised the thickness *d* and etched diameter *b* of the GaAs layer, and obtained the optimal size to form the mode filter. For our further research, the number of transverse modes is calculated when the diameter of the oxide aperture is $2a = 6 \mu m$. Then, the dependence of the reflectivity of the transverse mode in the p-type DBR on the thickness *d* and the etched diameter *b* of the GaAs layer is investigated.

Figure 3. (*1*) Refractive index and (*2*) normalised optical field intensity of the VCSEL with (a) λ and (b) $\lambda/2$ -cavities.

According to the fibre model theory, the normalised frequency *V* of the VCSEL can be written in the form

$$
V = k_0 a \sqrt{n_1^2 - n_2^2},\tag{1}
$$

where k_0 is the vacuum wave vector [29]. The normalised frequency is the characteristic frequency of the LP*mn* mode in the optical waveguide [29]. At $k_0 = 2\pi/\lambda$ ($\lambda = 940$ nm), $a = 3$ µm, $n_1 = 3.310$, and $n_2 = 3.303$, *V* is calculated to be 4.31. According to the relationship between the characteristic frequency and the number of LPmn modes, we conclude that there are four transverse modes, LP_{01} , LP_{11} , LP_{21} and LP_{02} in the cavity of the VCSEL, as shown in Figs 4a –4d.

The mechanism of the mode filter is to increase the difference of the reflectivity between the fundamental transverse mode and the higher-order transverse mode in the p-type DBR to achieve the mode selection. Thus, we need to optimise the thickness of the GaAs layer to minimise the reflectivity of the four transverse modes, and then etch the GaAs layer in the distribution region of the LP_{01} mode to form the mode filter [17]. In this case, the LP_{01} mode first reaches the emission condition due to the high reflectivity, while the other three high-order transverse modes cannot be emitted due to lower reflectivity (higher threshold current) under the same condition.

Figure 5a shows the relationship between the reflectivities of the four transverse modes and the thickness of the GaAs layer. As the thickness of the GaAs layer increases from 10 nm to 160 nm, the reflectivity of each transverse mode

Figure 4. (Colour online) Distribution of modes with an oxide aperture of 6 μ m: (a) LP₀₁, (b) LP₁₁, (c) LP₂₁, and (d) LP₀₂.

decreases firstly, and reaches a minimum at about 80 nm, then increases rapidly. According to the results, the thickness *d* of the mode filter is chosen to be 80 nm.

Figure 5b shows the relationship between the reflectivities of the four transverse modes and the etched diameter of the GaAs layer. By increasing the etched diameter of the GaAs layer from 0.5 to 5.5 µm, the reflectivities of the four transverse modes increase gradually, but the curves are not consistent. The reason is that the four transverse modes are differently distributed in the VCSEL cavity, and so the effect of the mode filter on the reflectivities of the four transverse modes is different at a same etched diameter.

In order to make the mode filter only suppress the higherorder transverse modes, the overlapping area (centre area) of the GaAs layer and the fundamental transverse mode must be etched away, with the other areas remaining without any changes. Therefore, we need to obtain an optimal etched diameter for the mode filter to maximize the difference of the reflectivities between the fundamental transverse mode and the three higher-order transverse modes. In this case, LP_{01} first reaches the emission condition, while the other three high-order transverse modes cannot be generated because of their lower reflectivities [32].

The difference of the reflectivities between the fundamental transverse mode and the three higher-order transverse modes as a function of the etched diameter of GaAs layer is plotted in Fig. 6. When the etched diameter of the GaAs layer is less than $2.5 \mu m$, the difference of the reflectivities between LP_{01} and LP_{11} , LP_{21} increases. After the etched diameter

exceeds $2.5 \mu m$, the difference of the reflectivities tends to decrease, and the maximum value is obtained at $2.5 \mu m$, which is 0.14% and 0.21%, respectively. The difference of the reflectivity between LP_{01} and LP_{02} reaches a maximum of 0.31% at the etched diameter of 3.5 μ m. We also simulated the emission spectrum of the VCSEL, and obtained the side mode suppression ratio of 90 dB, which meets the requirements of the single-transverse-mode output. After comprehensive consideration, the etched diameter *b* of the mode filter is chosen to be $2.5 \text{ }\mu\text{m}$ [32].

4. Conclusions

We have investigated a 940-nm VCSEL with *l*/2-cavity length and mode-filter structure. The cavity length of the VCSEL is designed to be $\lambda/2$, which increases the optical confinement factor by 12.5% (from 3.01% to 3.44%) and reduces the threshold current reduces by 30.8% (from 0.85 mA to 0.65 mA). Then, the GaAs layer is added on the p-type DBR as a mode filter to control the single-transverse-mode output. The optimal thickness and etched diameter of the mode filter are obtained to be 80 nm and $2.5 \mu m$, respectively. In addition, the side-mode suppression ratio reaches 90 dB, which meets the requirement of the single-transverse-mode output. We believe that our theoretical investigation and structure optimisation can provide solutions for fabricating high-quality VCSELs based on 3D recognition, and it will potentially improve the development of 940-nm VCSELs in the field of consumer electronics.

Figure 5. Dependences of the reflectivity on (a) the GaAs layer thickness and (b) etched diameter for different transverse modes.

Figure 6. Difference of the reflectivity of modes vs. etched diameter.

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